

Customer No.: 31561
Application No.: 10/065,917
Docket No.: 9023-US-PA

AMENDMENTS

Please amend the application as indicated hereafter.

In the Claims:

Please amend the claims according to the following listing of claims and substitute it for all prior versions and listings of claims in the application.

1. (currently amended) A digital-to-analog converting circuit applied to a current-type data driver of a display and operative to generate a data current to drive a plurality of pixels of the display, characterized in comprising a plurality of transistors, wherein the ratio of channel-width to channel-length of each transistor is identical.

2. (previously presented) The digital-to-analog converting circuit according to claim 1, further comprising a control device coupled to the transistors to receive a plurality of data bits, so as to control a conducting number among the transistors according to the data bits.

3. (Cancelled)

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4. (currently amended) A digital-to-analog converting circuit, applied to a current-type data driver and operative to generate a data current to drive a plurality of pixels of a display, comprising:

a plurality of transistors; and

a control device coupled to the transistors, the control device being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits;

wherein the ratio of the channel-width to channel-length for each of the transistors is the same.

5. (Cancelled)

6. (previously presented) A digital-to-analog converting circuit, suitable for use in a current-type data driver of a display, the digital-to-analog converting circuit characterized in comprising a plurality of transistors, the ratio of channel-width to channel-length of each of the transistors being the same, wherein a source/drain terminal of each of the transistors is coupled to an operation voltage, the digital-to-analog converting circuit being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits to generate an current in accordance with the data bits.

7. (previously presented) A digital-to-analog converting circuit, suitable for use in a current-type data driver of a display, the digital-to-analog converting circuit comprising:

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a control device; and

a plurality of transistors, the ratio of channel-width to channel-length of each of the transistors being the same, wherein a source/drain terminal of each of the transistors is coupled to the control device and a gate terminal ~~another source/drain terminal~~ of each of the transistors is coupled to an operation voltage, the control device being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits to generate a current in accordance with the data bits.